



SECTION 2. FORMS PTO/SB/08A and 08B (formerly Form PTO-1449)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Titus, et al

Attorney Docket: 2550/196

Serial No: 10/786,247

Art Group Unit: 2816

Date Filed: 02/25/04

Examiner Name: Wells

Invention: An Improved MOS Varactor for LC VCOs

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE STATEMENT

U.S. PATENT DOCUMENTS					
Examiner Initials	Reference Number	Document Number	Issue Date	Inventor	Class/Subclass
EW	AA	US6,407,412 B1	06/18/02	Iniewski, et al	257/107

OTHER DOCUMENTS			
Examiner Initials	Reference Number	Author	Title of Article, Title of Journal, Volume Number, Page Numbers, Date
ICM	AB	Andreani, P., et al	"On the Use of MOS Varactors in RF VCO's", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 35, No. 6, June 2000, pp. 905-910
↓	AC	Bunch, R. L., et al	"Large-Signal Analysis of MOS Varactors in CMOS-G LC VCOs", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 38, No. 8, August 2003, pp. 1325-1332
↓	AD	Maget, J., et al	"Influence of Novel MOS Varactors on the Performance of a Fully Integrated UMTS VCO in Standard 0.25- μ m CMOS Technology", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 37, No. 7, July 2002, pp. 953-958
↓	AE	Typpo, J.	"A Back Gate Tuned PFET VCO", <i>Analog Integrated Circuits and Signal Processing</i> , 30, 265-269, 2002

Examiner Signature: _____

WELLS

Date Considered: _____

8/19/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation *if not* in conformance and not considered. Include copy of this form with next communication to applicant.